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To:	Examiner Thomas L Dickey	From:	Timothy N. Trop
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Applicant:

Brian G. Johnson

Art Unit:

2826

Serial No.:

10/633,874

Examiner:

Thomas L. Dickey

Filed:

August 4, 2003

Docket:

ITO.0047US

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P16202

For:

Lateral Phase Change

Memory

Assignee:

Intel Corporation

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed April 10, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: May 4, 2006

I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the

date/indicated above.

Cynthia K. Hayden